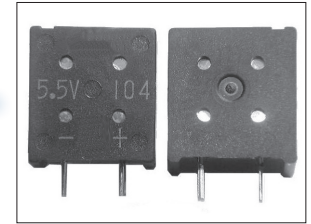


## FEATURES

- DOUBLE LAYER CONSTRUCTION
- LONG DURATION POWER BACK-UP FOR CMOS RAM (UP TO 50 $\mu$ A DISCHARGE CURRENT)
- WIDE OPERATING TEMPERATURE (-40°C to +85°C)
- OPTIONAL TAPED PACKAGING FOR AUTOMATED INSERTION
- SUITABLE FOR FLOW SOLDERING
- UL94V-0 MOLDED CASE
- LEAD-FREE FINISH

**RoHS Compliant**  
includes all homogeneous materials

\*See Part Number System for Details



## CHARACTERISTICS

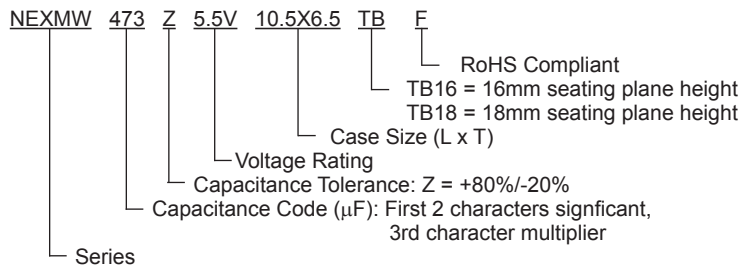
Rated Voltage Range	3.5 ~ 5.5VDC	
Rated Capacitance Range	0.047F ~ 0.47F (47,000 $\mu$ F ~ 470,000 $\mu$ F)	
Operating Temp. Range	-40°C ~ +85°C	
Capacitance Tolerance	+80%/-20% (Z)	
Load Life Test @ 85°C 1000 hours	$\Delta$ Capacitance Change	Less than $\pm$ 30% of initial measured value
	Maximum ESR	Less than 200% of the specified maximum value
	Current at 30 minutes	Less than 200% of the specified maximum value
Temperature Cycling (5 cycles, -40 ~ +85°C)	$\Delta$ Capacitance Change	Within +80%/-20% of specified value
	Maximum ESR	Less than specified maximum value
	Current at 30 minutes	Less than specified maximum value
Humidity Resistance (240 hours @ 40°C/90% RH)	$\Delta$ Capacitance Change	Less than $\pm$ 20% of initial measured value
	Maximum ESR	Less than 120% of the specified maximum value
	Current at 30 minutes	Less than 120% of the specified maximum value

Super Capacitor  
Application Guide

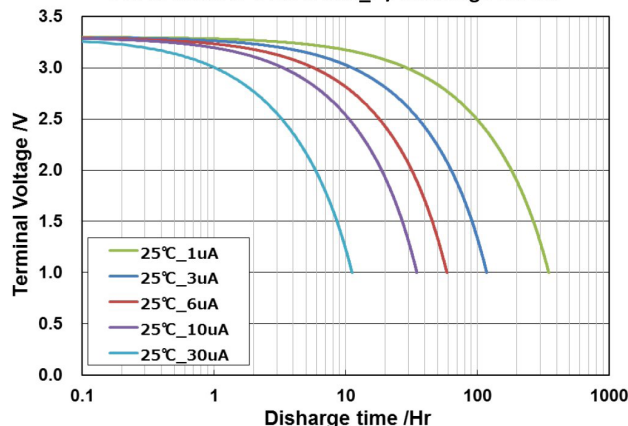
## STANDARD VALUES AND SPECIFICATIONS

NIC P/N	Capacitance Value (F)		Rated Voltage (VDC)	Max. Current @ 30 minutes (mA)	Max. ESR @ 1KHz ( $\Omega$ )	Voltage Holding (V)
	Charge	Discharge				
NEXMW104Z3.5V10.5X6.5TBF	0.100	-	3.5	0.090	50	-
NEXMW474Z3.6V14X9TBF	0.470	-	3.6	0.42	25	-
NEXMW473Z5.5V10.5X6.5TBF	0.047	0.062	5.5	0.071	200	4.2
NEXMW104Z5.5V10.5X6.5TBF	0.100	-	5.5	0.150	50	4.2

## PART NUMBERING SYSTEM



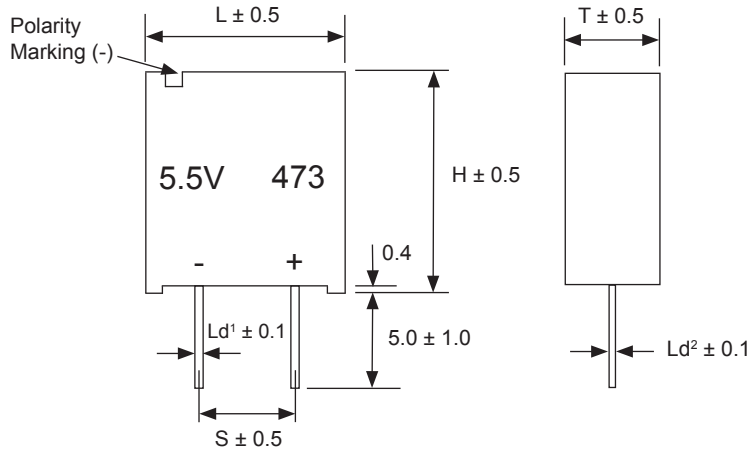
PN: NEXMW474Z3.6V14X9\_F / Discharge Curves



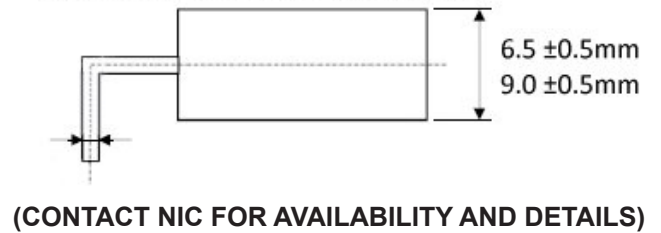
## CASE DIMENSIONS (mm)

NIC P/N	DIMENSIONS (mm)					
	L	H	S	T	Ld <sup>1</sup>	Ld <sup>2</sup>
NEXMW104Z3.5V10.5X6.5TBF	10.5	11.5	5.0	6.5	0.5	0.4
NEXMW474Z3.6V14X9TBF	14.0	15.0	5.0	9.0	0.6	0.6
NEXMW473Z5.5V10.5X6.5TBF	10.5	11.5	5.0	6.5	0.5	0.4
NEXMW104Z5.5V10.5X6.5TBF	10.5	11.5	5.0	6.5	0.5	0.4

## STANDARD BULK PACKAGING CONFIGURATION



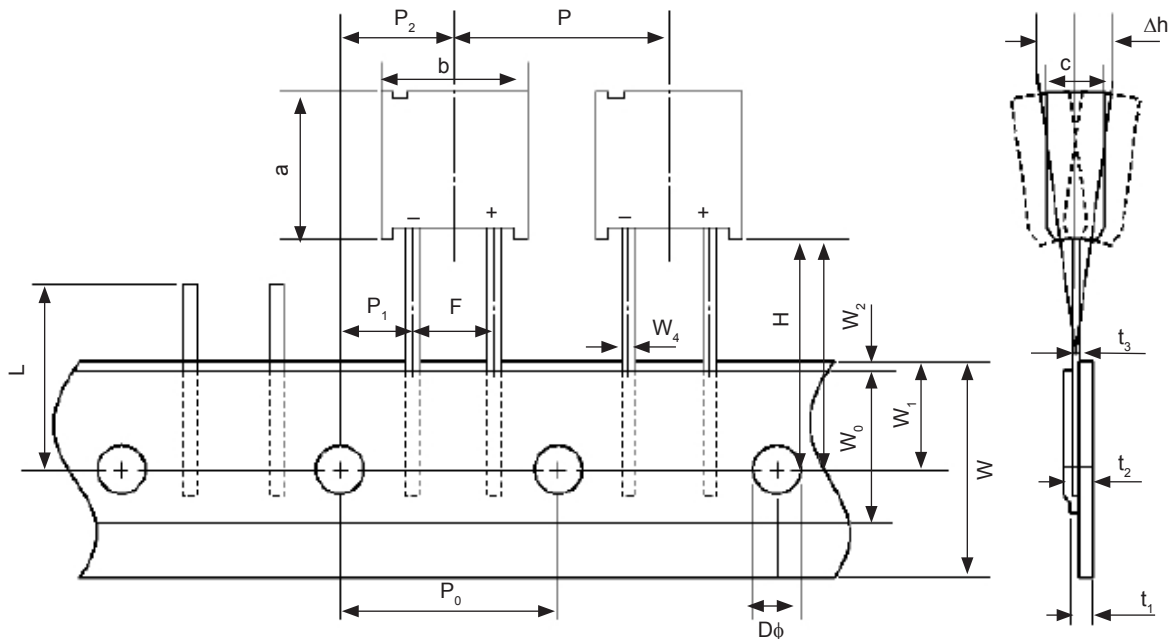
## OPTIONAL LOW PROFILE, HORIZONTAL MOUNT



## TAPING SPECIFICATIONS (mm)

a	b	c	W <sub>4</sub>	t <sub>3</sub>	P	P <sub>0</sub>	P <sub>1</sub>	P <sub>2</sub>	F	Δh	W	W <sub>0</sub>	W <sub>1</sub>	W <sub>2</sub>	H	D <sub>0</sub>	t <sub>1</sub>	t <sub>2</sub>	L
±0.5	±0.5	±0.5	±0.1	±0.1	±1.0	±0.3	±0.7	±1.3	±0.5	max.	<sup>+1/-0.5</sup>	min.	±0.5	max.	±0.5	±0.2	±0.2	max.	max.
11.5	10.5	6.5	0.5	0.4	12.7	12.7	3.85	6.35	5.0	2.0	18	12.5	9.0	3.0	16/18	φ4.0	0.7	1.5	11.0
15.0*	14.0*	9.0*	0.6*	0.6*		25.4*									0.67*		1.7*		

\*NEXMW474Z3.5V14X9TBF



## PRECAUTIONS

Please review the notes on correct use, safety and precautions found at [https://www.nicomp.com/resource/files/double/Double\\_Layer\\_Capacitor\\_Guide\\_0810-RevBrA7.pdf](https://www.nicomp.com/resource/files/double/Double_Layer_Capacitor_Guide_0810-RevBrA7.pdf)  
 If in doubt or uncertainty, please review your specific application - process details with NIC's technical support personnel: [tpmg@nicomp.com](mailto:tpmg@nicomp.com)

